

Notice of References Cited	Application/Control No. 09/776,080	Applicant(s)/Patent Under Reexamination HSU ET AL.	
	Examiner Chat C. Do	Art Unit 2124	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,421,696	07-2002	Horton, David C.	708/404
	B	US-5,974,515	10-1999	Bachmat et al.	711/170
	C	US-6,018,351	01-2000	Mennemeier et al.	345/654
	D	US-6,067,099	05-2000	Kabir et al.	345/561
	E	US-5,815,421	09-1998	Dulong et al.	708/520
	F	US-6,141,673	10-2000	Thayer et al.	708/402
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Yung-Pin et al., A Cost-Effective Architecture for 8x8 Two-Dimensional DCT/IDCT Using Direct Method, 1997, IEEE Transactions on Circuits and Systems for Video Technology, Vol. 7, No. 3, page 459-467.
	V	Kuo-Hsing et al., The Design and Implementation of DCT/IDCT Chip with Novel Architecture, 2000, IEEE International Symposium on Circuits and Systems, pages IV-741-IV-744.
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.